

Minutes of Virtual BoS Meeting

Date: 08.11.2020

Time: 10:30AM

Agenda:

1. Approval of Scheme of Instruction & Evaluation for M. Tech (VLSI & ES)-I, II, III & IV Semesters under PRR-20.
2. Approval of Syllabi of M. Tech. (VLSI & ES)- I, II, III & IV Semesters courses under PRR-20.
3. Approval of PEOs and PSOs to be adopted in the finalised scheme and syllabi of M. Tech (VLSI&ES) I - IV semesters under URR-20.
4. Any other item with the permission of chair

Members Present:

Virtual BoS meeting was conducted on 08.11.2020, from 10:30 am to 1:00 pm through ZOOM application and the following members were present during the meeting.

S. No.	Name of the Member	Designation	Position in BoS	Signature
1.	Dr. K. Venu Madhav	<i>Professor & HoD, EIED, KITSW</i>	<i>Chairperson, BoS</i>	
2.	Dr. K. Sivani	<i>Senior Professor of EIED, KITSW</i>	<i>Member</i>	
3.	Prof. M. Sreelatha	<i>Professor of EIED, KITSW</i>	<i>Member</i>	
4	Smt. R. Nirmala Devi	<i>Assoc. Prof. of EIED, KITSW</i>	<i>Member</i>	
5.	Sri O. Anjaneyulu	<i>Assoc. Prof. of EIED, KITSW</i>	<i>Member</i>	
6.	Dr. P. Laxminarayana	<i>Director, NERTU, Osmania University, Hyderabad</i>	<i>External Member (from renowned Academic Institute)</i>	
7.	Dr. T. Kishore Kumar	<i>Professor of ECE, NIT, Warangal</i>	<i>External Member (from renowned Academic Institute)</i>	
8.	Dr. K. Bikshalu	<i>Asst. Prof. of ECE, University College of Engineering, KU, Kothagudem</i>	<i>External Member (University Nominee)</i>	
9.	Sri T. Srinivas	<i>CEO, Sumith Electronics Pvt. Ltd., Hyderabad</i>	<i>External Member (from Industry)</i>	
10.	Dr. Y. Jagan Mohan Reddy	<i>Senior Engineering Manager, Honeywell Technology Solutions Lab Pvt. Ltd., Hyderabad</i>	<i>External Member (from Industry)</i>	
11.	Sri R. Dileep	<i>Senior ASIC design Engineer, Synopsis Inc. Pvt. Ltd., Hyderabad</i>	<i>External Member (from Industry)</i>	
12.	Sri R. Divyanand	<i>Senior Engineer, Qualcomm Technology Inc., Hyderabad</i>	<i>External Member (Post Graduate Meritorious Alumnus- Academia/Industry)</i>	

13.	Sri M. Raghuram	Assoc. Prof. of EIED, KITSW	Co-OptedMember-1	
14.	Sri B. Shashikanth	Asst. Prof. of EIED, KITSW	Co-OptedMember-2	
15.	Sri B. Jeevan	Asst. Prof. of EIED, KITSW	Co-OptedMember-3	

Resolutions:

1. Prof. K. Venu Madhav, HoD-EIE & Chairperson BoS-EIE welcomed all the external members to the virtual meeting and introduced all the internal members of BoS-EIE.
2. Chairperson BoS-EIE briefed the agenda points to be discussed during the BoS meeting.
3. All the external BoS members participated in the meeting and offered their valuable suggestions on:
 - (i) Scheme of Instructions & Evaluation for M. Tech. (VLSI & ES) -I, II, III & IV Semesters under PRR-20
 - (ii) Syllabi of M. Tech. (VLSI & ES) -I, II, III & IV Semesters courses
 - (iii) PEOs and PSOs to be adopted in the final scheme and syllabi of M. Tech (VLSI&ES) I - IV semesters under URR-20
4. As per External BoS members' suggestions, the modifications were incorporated in the scheme & syllabi and the same are tabulated below:

S. No.	Course Code	Course Name	Suggestions made by the BoS members	Remarks
I SEMESTER:				
1.	P20VE102	Professional Core-2: Design with ARM processor	Change the title, appropriately	Title is now changed to: "Microcontroller based Embedded Systems"
2.	P20VE106	Professional Core Lab-II: (Based on Professional Core- 2) Design with ARM processor Laboratory	Change the title, appropriately	Title is now changed to: "Microcontroller based Embedded Systems Laboratory"
II SEMESTER:				
3.	P20VE202	Professional Core-4: Internet of Things with Python	Need to Change title To "Internet of things "	Title is now changed to: "Internet of things"
4.	P20VE203 C	Wireless & Mobile Communications	Subject title is not appropriate, apt to have title as "5G applications in embedded systems"	Title is now changed to: "Wireless Technologies in Embedded systems"
5.	P20VE204 B	Industrial Automation	Subject title is not appropriate, apt to have title as "Industrial IOT"	Title is now changed to: "Industrial IOT" Syllabus is restructured to suit "Industrial IOT"

6.	P20VE204 C	VLSI Signal Processing	Need to change title as “VLSI DSP Architectures”	Title is now changed to: “VLSI DSP Architectures”

5. Other important suggestions given by the external BoS members:

S. No.	Suggestions made by the external BoS members	Remarks
1.	<i>Prof. T. Kishore Kumar, ECED, NITW:</i>	i) Syllabus/Title should be generic not specific ii) Comprehensive viva-voce is necessary iii) Student to present one paper along with project dissertation iv) Use action verbs in learning outcomes v) Mini Project shall be conducted with comprehensive viva
2.	<i>Prof. P. Laxminarayana, Director, NERTU-OU:</i>	i) Board based design is essential for Embedded systems ii) HMI topic can be included in Embedded systems
3.	<i>Dr. Y. Jagan Mohan Reddy, Senior Engineering Manager, Honeywell, Hyderabad</i>	i) Edge computing shall be given due importance considering latencies, while dealing with IOT
4.	<i>Sri R. Dileep, Sr. ASIC Design Engineer, Synopsis:</i>	i) Orientation programme is necessary for students before the start of the M.Tech. class work, so that the students can choose appropriate electives

Meeting was concluded by thanking all the BoS members, Principal, AAC members and faculty of EIE Department for giving their suggestions while preparing the scheme and drafting the syllabus.

The meeting was adjourned at 1:00pm

Regards:

Dr. K. Venu Madhav

Chairperson, BoS-EIE, KITSW & Professor & Head, Department of EIE,

*Kakatiya Institute of Technology & Science(KITSW), Yerragattu Hillock, Bheemaram,
Warangal, Telangana, India-506015.*

Mobile: +91- 9032072025; Email: hod.eie@kitsw.ac.in